* Board schematics: complete
* Design document: incomplete
  + Abstract incomplete
  + Design requirements incomplete
  + Design Overview incomplete
  + Previous design incomplete
    - Hydrodynamix
  + Implementations incomplete
    - Power System incomplete
      * Schematic
      * Part descriptions
    - Active Filter incomplete
      * Filter response diagrams
      * Part selection
    - Amplification stage incomplete
      * Part selection
      * Gain choice
      * Gain switching chip
    - ADC sampling incomplete
      * MAXIM 11043
      * Reference the design choices
  + Known issues incomplete
    - Many lol
  + Appendix incomplete
    - Reference parts
    - Reference Python package
    - Reference fpga project